#9 Jandte 4-30-02 KRAKNELL

500.26967RC1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

KATSURA, et al

Serial No.:

09/536,646

Filed:

March 28, 2000

For:

GRAPHIC PROCESSING APPARATUS UTILIZING IMPROVED DATA

TRANSFER TO REDUCE MEMORY SIZE

Group:

2671

Examiner:

U. Chauhan

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APR 2 9 2002

[Technology Center 2600

<u>AMENDMENT</u>

Assistant Commissioner for Patents Washington, D.C. 20231

April 24, 2002

Sir:

In response to the Office Action dated October 24, 2001, please amend the above-identified application as follows.

IN THE CLAIMS

Please cancel claims 1-8 without prejudice or disclaimer of the subject matter thereof.

[1. A graphic processing apparatus comprising:

memory means, including a plurality of memory locations in an array of columns, having corresponding column addresses, and rows, having corresponding row addresses, for storing data;

data processing means for specifying a row address in said memory means for retrieval of data from the memory locations at the different column addresses within the